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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/690,204	
	Filing Date	October 20, 2003	
	First Named Inventor	Chih Hsin Wang et al.	
	Art Unit	2818	
	Examiner Name	Not assigned yet	
Total Number of Pages in This Submission	5	Attorney Docket Number	2102397-911401

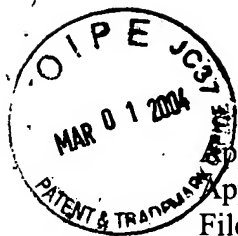
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Signature	
Date	February 27, 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/690,204  
Applicant : Chih Hsin Wang et al.  
Filed : October 20, 2003  
TC/A.U. : 2818  
Examiner : Not assigned yet  
Title : Semiconductor Memory Array Of Floating Gate Memory Cells  
With Low Resistance Source Regions And High Source Coupling

Docket No. : 2102397-911401  
Customer No. : 26379

Non-Fee Amendment  
Commissioner for Patents  
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GRAY CARY WARE & FREIDENRICH Date: 2/27/04

By: Kathleen LaBrie  
Kathleen LaBrie

**PRELIMINARY AMENDMENT**

Sir:

Please preliminarily amend the above identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 3 of this paper.